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				Application Number	TBD
				Filing Date	Herewith
				First Named Inventor	Andrew W. Lueck
				Group Art Unit	TBD
				Examiner Name	Not Assigned
Sheet	1	of	1	Attorney Docket No.	TI-35560

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	CA	PCI EXPRESS BASE SPECIFICATION, REV. 1.0, Isochronous Applications, pages 395-405.	
	CB	PCI EXPRESS BASE SPECIFICATION, REV. 1.0, Virtual Channel Capability, pages 372-387.	
	CC	PCI Express-SIG Developers Conference, Board Design Guidelines for PCI Express™ Interconnect, Intel Corporation, pages 1-4.	
	CD	wysiwyg://fraContent.fraRightFrame.109/h.../hh/buses/1394-isoch_227b.asp?frame=true, MSDN FLASH NEWSLETTER, System Support for Buses: Windows DDK, Setting up Isochronous Transfer for IEEE 1394 Devices, July 24, 2002, pages 1-3	
	CE	wysiwyg://fraContent.fraRightFrame.120/h.../hh/buses/1394-isoch_2kkn.asp?frame=true, MSDN FLASH NEWSLETTER, System Support for Buses: Windows DDK, Isochronous I/O for IEEE 1394 Devices, July 24, 2002, page 1.	
	CF	http://www.osr.com/ddk/buses/1394-isoch_74pz.htm, Completing an Isochronous Data Transfer, 05 September 2002, page 1.	
	CG	wysiwyg://157/http://whatIs.techtarget.com/definition/0..._sid9_gci748584,00.html, WhatIs.com, IEEE 1394, Jun 18, 2001, pages 1-3.	
	CH		

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